

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10002217
	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01207M	

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1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	<input type="checkbox"/>
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19	USPTO Final Rejection for 09/989,765 (CD01194M) dated 03/31/09; 18 pages	<input type="checkbox"/>
20	CD00184; "Host to FPGA Interface in an In-Circuit Emulation System;" 10/10/01; 09/975,105; Nemecek; 44 pages	<input type="checkbox"/>
21	Anonymous; "Using Debug"; 1999; Prentice-Hall Publishing; 20 pages	<input type="checkbox"/>
22	Harrison et al.; "Xilinx FPGA Design in a Group Environment Using VHDS and Synthesis Tools"; Colloquium on Digital System Design Using Synthesis Techniques; February 15, 1996; 4 pages	<input type="checkbox"/>

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23	Microsoft Press Computer User's Dictionary; 1998; 3 pages	<input type="checkbox"/>
24	Sreeram Duvvuru and Siamak Arya, "Evaluation of a Branch Target Address Cache," 1995; IEEE, pages 173-180	<input type="checkbox"/>
25	Andrew S. Tanenbaum with contributions from James R. Goodman, "Structured Computer Organization," 1999, Prentice Hall, Fourth Edition	<input type="checkbox"/>
26	CD00186; "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events;" October 10, 2001; 09/975,338; Nemecek et al.; 34 pages	<input type="checkbox"/>
27	USPTO Final Rejection for 09/994,601 (CD01170) dated 05/18/2007; 17 pages	<input type="checkbox"/>
28	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 03/02/2006; 17 pages	<input type="checkbox"/>
29	ITO, SERGIO AKIRA AND CARRO, LUIGI; "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000; 6 pages	<input type="checkbox"/>
30	Julio Faura et al.; "A NOVEL MIXED SIGNAL PROGRAMMABLE DEVICE WITH ON-CHIP MICROPROCESSOR", 1997, IEEE 1997 Custom Integrated Circuits Conference; 4 pages	<input type="checkbox"/>
31	Monte Mar, Bert Sullam, Eric Blom; "An architecture for a configurable Mixed-signal device", 2003, IEEE Journal of Solid-State Circuits, Volume 3; 4 pages	<input type="checkbox"/>
32	ROBINSON, GORDON D; "Why 1149.1 (JTAG) Really Works", May 1994, Conference Proceedings Electro/94 International, 10-12 May 1994, Combined Volumes; 6 pages	<input type="checkbox"/>
33	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001; 25 pages	<input type="checkbox"/>

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34	"PSoC technology complete changes 8-bit MCU system design", Cypress Microsystems, Inc. retrieved from <http://www.archive.org/web/20010219005250/http://cypressmicro.com-/t...>, Feb. 19, 2001; 21 pages	<input type="checkbox"/>
35	Specks et al., "A Mixed Digital-Analog 16B Microcontroller with 0.5MB Flash Memory, On-Chip Power Supply, Physical Network Interface, and 40V I/O for Automotive Single-Chip Mechatronics," IEEE, Feb. 9, 2000; 1 page	<input type="checkbox"/>
36	Hsieh et al., "Modeling Micro-Controller Peripherals for High-Level Co-Simulation and Synthesis," IEEE, 1997; 4 pages	<input type="checkbox"/>

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